

DAFTAR ISI

	Halaman
LEMBAR JUDUL	i
LEMBAR PERNYATAAN	ii
LEMBAR PENGESAHAN	iii
ABSTRAK	iv
KATA PENGANTAR	v
DAFTAR ISI	viii
DAFTAR GAMBAR	x
DAFTAR TABEL	xii
BAB I PENDAHULUAN	1
1.1 Latar Belakang	1
1.2 Rumusan Masalah	2
1.3 Batasan Masalah	2
1.4 Tujuan Penelitian.....	2
1.5 Metodologi Penelitian	3
1.6 Sistematika Penulisan	3
BAB II LANDASAN TEORI	5
2.1 Studi Literatur	5
2.1.1 <i>Design and Contruction of mobile phone based home and office appliances remot switching system</i>	5
2.1.2 <i>DTMF based Home Automation System. International Journal of Advanced Research in Electrial, Elecronic and Instrumentation Engineering</i>	5
2.1.3 <i>Appliances control using mobile phone. partical fulfillment of therequirement B.sc. The University of Oemar Al-mukhtar, Collage of Engineering Electrical and Electronic Engineering Departement</i>	6
2.2 GSM (<i>Global System for Mobile Communication</i>)	6
2.3 Ponesl (<i>Handphone</i>)	7

2.4	<i>DTMF Decoder (Dual Tone Multiple Frequency)</i>	9
2.5	<i>Demultiplexer (Demux)</i>	10
2.6	<i>Relay</i>	12
2.6.1	<i>Relay Latching</i>	13
2.6.2	<i>Relay SPST</i>	14
2.6.3	<i>Relay SPDT</i>	15
2.6	<i>Sistem Pompa Bensin Injeksi (Injection Fuel Pump)</i>	15
BAB III METEDOLOGI PELAKSANAAN		17
3.1	<i>Alir Pembuatan Diagram</i>	17
3.2	<i>Perancangan Alat</i>	18
3.2.1	<i>Blok Interface</i>	19
3.2.2	<i>Blok Alat</i>	19
3.2.2.1	<i>Blok DTMF Decoder</i>	20
3.2.2.2	<i>Blok Demultiplexer</i>	21
3.2.2.3	<i>Relay Switching</i>	22
3.2.3	<i>Perangkat</i>	24
3.3	<i>Cara Kerja Alat</i>	25
BAB IV HASIL DAN PEMBAHASAN		28
4.1	<i>Pengujian Dan Analisis Setiap Blok</i>	28
4.1.1	<i>Pengujian Blok DTMF Decoder</i>	28
4.1.1.1	<i>Pengujian Level Tegangan Keluaran DTMF Decoder</i>	26
4.1.1.2	<i>Pengujian Level Arus Keluaran DTMF Decoder</i>	30
4.1.2	<i>Pengujian Blok Demultiplexer</i>	31
4.1.2.1	<i>Pengujian Level Tegangan Keluaran Demutipxer</i>	31
4.1.2.2	<i>Pengujian Level Arus Keluaran Demutipxer</i>	33
4.1.3	<i>Pengujian Blok Rangkaian Relay Switching</i>	34
4.1.3.1	<i>Pengujian Level Tegangan Keluaran Relay Switching</i>	34
4.1.3.2	<i>Pengujian Level Arus Keluaran Relay Switching</i>	37
4.2	<i>Pengujian Dan Analisis Blok Sistem Keseluruhan</i>	39
4.2.1	<i>Pengujian Level Tegangan Pada Blok Sistem Keseluruhan</i>	39
4.2.2	<i>Pengujian Level Arus Pada Blok Sistem Keseluruhan</i>	42

BAB V PENUTUP	45
5.1 Kesimpulan	45
5.2 Saran	45
DAFTAR PUSTAKA	xiv
LAMPIRAN A	
LAMPIRAN B	

